	ed States Paten	TAND TRADEMARK OFFICE	UNITED STATES DEPAR United States Patent and Address: COMMISSIONER F P.O. Box 1450 Alexandria, Virginia 22: www.uspto.gov	
APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/686,331	10/14/2003	Richard M. Butler	10991268-3	7201
22879 7590 02/06/2008 HEWLETT PACKARD COMPANY P O BOX 272400, 3404 E. HARMONY ROAD INTELLECTUAL PROPERTY ADMINISTRATION FORT COLLINS, CO 80527-2400			EXAMINER	
			DO, CHAT C	
			ART UNIT	PAPER NUMBER
		· · ·	2193	
			NOTIFICATION DATE	DELIVERY MODE

02/06/2008 ELECTRONIC

## Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

JERRY.SHORMA@HP.COM mkraft@hp.com ipa.mail@hp.com

4 -	Application No.	Applicant(s)
Advisory Action	10/686,331	BUTLER, RICHARD M.
Before the Filing of an Appeal Brief	Examiner	Art Unit
	Chat C. Do	2193
The MAILING DATE of this communication appe	ears on the cover sheet with the o	correspondence address
HE REPLY FILED <u>14 January 2008</u> FAILS TO PLACE THIS A	APPLICATION IN CONDITION FOR	R ALLOWANCE.
<ul> <li>Image: The reply was filed after a final rejection, but prior to or or this application, applicant must timely file one of the follow places the application in condition for allowance; (2) a Nor a Request for Continued Examination (RCE) in compliant time periods:</li> <li>a) The period for reply expiresmonths from the mailing the period for reply expires</li></ul>	wing replies: (1) an amendment, af otice of Appeal (with appeal fee) in ce with 37 CFR 1.114. The reply m	fidavit, or other evidence, which compliance with 37 CFR 41.31; or (3
b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire I Examiner Note: If box 1 is checked, check either box (a) or TWO MONTHS OF THE FINAL REJECTION. See MPEP 7 Extensions of time may be obtained under 37 CFR 1.136(a). The date	Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailin (b). ONLY CHECK BOX (b) WHEN TH 06.07(f).	ig date of the final rejection. E FIRST REPLY WAS FILED WITHIN
ave been filed is the date for purposes of determining the period of ex inder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the et forth in (b) above, if checked. Any reply received by the Office later hay reduce any earned patent term adjustment. See 37 CFR 1.704(b) NOTICE OF APPEAL	tension and the corresponding amount shortened statutory period for reply orig r than three months after the mailing da	of the fee. The appropriate extension fee jinally set in the final Office action; or (2) a
The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exte a Notice of Appeal has been filed, any reply must be filed <u>MENDMENTS</u>	nsion thereof (37 CFR 41.37(e)), to within the time period set forth in 3	o avoid dismissal of the appeal. Since 37 CFR 41.37(a).
<ul> <li>The proposed amendment(s) filed after a final rejection,</li> <li>(a) They raise new issues that would require further co</li> <li>(b) They raise the issue of new matter (see NOTE below)</li> </ul>	nsideration and/or search (see NO	
(c) They are not deemed to place the application in be appeal; and/or		ducing or simplifying the issues for
(d) They present additional claims without canceling a NOTE: (See 37 CFR 1.116 and 41.33(a)).		jected claims.
. The amendments are not in compliance with 37 CFR 1.1	21. See attached Notice of Non-Co	ompliant Amendment (PTOL-324).
. Applicant's reply has overcome the following rejection(s)		
<ul> <li>Newly proposed or amended claim(s) would be al non-allowable claim(s).</li> </ul>		,
<ul> <li>For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is pro The status of the claim(s) is (or will be) as follows: Claim(s) allowed: <u>22</u>.</li> <li>Claim(s) objected to: <u>4-6 and 19</u>.</li> </ul>		
Claim(s) rejected: <u>1-3,7-18,20 and 21</u> . Claim(s) withdrawn from consideration:	• •	
<ul> <li>FFIDAVIT OR OTHER EVIDENCE</li> <li>The affidavit or other evidence filed after a final action, bu because applicant failed to provide a showing of good an was not earlier presented. See 37 CFR 1.116(e).</li> </ul>		
The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to o showing a good and sufficient reasons why it is necessar	overcome <u>all</u> rejections under appe y and was not earlier presented. S	al and/or appellant fails to provide a see 37 CFR 41.33(d)(1).
<ol> <li>The affidavit or other evidence is entered. An explanatio <u>REQUEST FOR RECONSIDERATION/OTHER</u></li> <li>The request for reconsideration has been considered bu <u>See Continuation Sheet.</u></li> </ol>		-
2. Note the attached Information Disclosure Statement(s).	(PTO/SB/08) Paper No(s)	
3. 🗌 Other:		HA
	U	Chat C. Do
		Examiner Art Unit: 2193

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## , Continuation Sheet (PTO-303)

Continuation of 11. does NOT place the application in condition for allowance because: The applicant argues in page 6 last paragraph to page 7 third paragraph that the cited reference by Koopman as primary reference fails to disclose the feature of "sampling data transmitted over a number of microprocessor buses at inputs" since the component 15 as recording device does not have a microprocessor and further the primary reference fails to disclose that the sample data is transmitted over a number of microprocessor buses.

The examiner respectfully submits that the previous rejection stands firmly based on broadly interpretation of the current claim language. First of all, the independent claim 1 does not require a particular structure of a microprocessor, but rather just a microprocessor. Second, the independent claim 1 does not define a type or a number of microprocessor buses. Based on the current language, the examiner had reasonably and broadly interpreted the above feature as a sampling device is sampling any data to/from or over a device to input digital data to MISRs wherein the device has a microprocessor as for capturing the noise source and the buses are the buses for transfering the captured noise sources to the MISRs.

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